

ABSTRACT OF THE INVENTION

The present invention is in the field of flash memory. More particularly, embodiments of the present invention may provide a negative voltage for erasing when
5 coupled to a memory cell to be erased and provide voltages to read or program when not coupled to a memory cell that is selected to be erased. Embodiments may also provide a high magnitude negative voltage to erase; a low impedance, low voltage current to read or program; and burn little to no current when not coupled to a memory cell that is selected to be erased.